



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

WARREN M. FARNWORTH
SALMAN AKRAM

Serial No.: 09/844,532

EXAMINER: Chang, R.

Filing Date: April 30, 2001

Art Unit: 3729

For: METHOD FOR FABRICATING A TEST INTERCONNECT
FOR BUMPED SEMICONDUCTOR COMPONENTS BY
FORMING RECESSES AND CANTILEVERED LEADS
ON A SUBSTRATE

Attorney Docket No. 97-1433.1

9-11-03
#13
RDS

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.56
August 15, 2003

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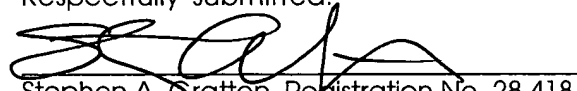
Sir:

The citations listed on the accompanying Form PTO/SB/08, copies attached, may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional material to the examination of this application does not exist, or that any one or more of these citations constitutes prior art under 35 USC §102.

It is requested that the above citations be made of record, if appropriate, in the prosecution of this application.

Respectfully submitted:


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CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: MAIL STOP AMENDMENT (NON FEE), Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450, on this 15th day of August, 2003.

August 15, 2003
Date of Signature


Stephen A. Gratton, Attorney of Record